

EAST Search History

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
S1	1	"20030126502"	US-PGPUB; USPAT	OR	OFF	2004/12/01 15:09
S2	5	LITT ADJ TIMOTHE.in.	USPAT	OR	OFF	2004/12/01 15:12
S4	1	S3 and boolean	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	OFF	2004/12/01 15:14
S3	10	LITT ADJ TIMOTHE.in.	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	OFF	2004/12/01 15:14
S5	5	S3 and word adj recognizer	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	OFF	2004/12/01 15:15
S7	473	"714"/\$.ccls. and logic with analyzer and compar\$5	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	OFF	2004/12/01 15:17
S6	642	"714"/\$.ccls. and logic with analyzer	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	OFF	2004/12/01 15:17
S11	4799	"714"/\$.ccls. and logic with analyzer with chip anf boolean adj logic	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	OFF	2004/12/01 15:27
S10	99	"714"/\$.ccls. and logic with analyzer with chip	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	OFF	2004/12/01 15:27
S12	6	"714"/\$.ccls. and logic with analyzer with chip AND boolean adj logic	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	OFF	2004/12/01 15:28
S8	46	"714"/\$.ccls. and logic with analyzer and compar\$5 and boolean	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	OFF	2004/12/01 15:35
S13	11	"714"/\$.ccls. and logic with analyzer with chip and compar\$5 and boolean	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	OFF	2004/12/01 15:37

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S16	25	"714"/\$.ccls. and logic with analyzer with chip and combinational	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	OFF	2004/12/01 15:48
S17	4	"714"/\$.ccls. and logic with analyzer with chip and word adj recognizer	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	OFF	2004/12/01 15:49
S14	11	"714"/\$.ccls. and logic with analyzer with chip and compar\$5 and boolean and count\$3	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	OFF	2004/12/01 15:58
S19	1304	"714"/\$.ccls. and combinational	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	OFF	2004/12/01 16:04
S20	128	"714"/\$.ccls. and combinational with compar\$5	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	OFF	2004/12/01 16:06
S21	103	"714"/\$.ccls. and combinational with compar\$5	USPAT	OR	OFF	2004/12/01 16:07
S22	16	"714"/\$.ccls. and combinational with compar\$5 and boolean	USPAT	OR	OFF	2004/12/01 16:08
S23	18	"714"/\$.ccls. and combinational with boolean with logic	USPAT	OR	ON	2004/12/01 16:11
S24	27	"714"/\$.ccls. and combinational with boolean	USPAT	OR	ON	2004/12/01 16:12
S25	233	"714"/\$.ccls. and combinational with scan	USPAT	OR	ON	2004/12/01 16:13
S26	197	"714"/\$.ccls. and combinational with scan and compar\$5	USPAT	OR	ON	2004/12/01 16:14
S27	19	"714"/\$.ccls. and combinational with scan WITH compar\$5	USPAT	OR	ON	2004/12/01 16:15
S29	657	"714"/\$.ccls. and combinational adj logic	USPAT	OR	ON	2004/12/01 16:17
S28	103	"714"/\$.ccls. and combinational WITH compar\$5	USPAT	OR	ON	2004/12/01 16:17
S30	6	"714"/\$.ccls. and combinational adj logic with boolean	USPAT	OR	ON	2004/12/01 16:18
S31	63	"714"/\$.ccls. and combinational adj logic with chip	USPAT	OR	ON	2004/12/01 16:24
S32	52	"714"/\$.ccls. and combinational adj logic with chip and scan	USPAT	OR	ON	2004/12/01 16:25

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S9	11	"714"/\$.ccls. and logic with analyzer with chip and compar\$5 and boolean	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	OFF	2004/12/01 16:31
S34	15	Hewlett-Packard.as. and logic with analyzer with chip	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	OFF	2004/12/01 16:33
S33	1	Hewlett-Packard.as. and logic with analyzer with chip and compar\$5 and boolean	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	OFF	2004/12/01 16:33
S35	15	Hewlett-Packard.as. and logic with analyzer with chip	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	OFF	2004/12/01 16:34
S15	65	"714"/\$.ccls. and logic with analyzer with chip and combination\$3	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	OFF	2004/12/01 16:39
S36	42	"714"/\$.ccls. and logic with analyzer with on-chip	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2004/12/01 16:49
S37	39	"714"/\$.ccls. and logic with analyzer with on-chip and count\$3	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2004/12/01 16:52
S38	35	"714"/\$.ccls. and logic with analyzer with on-chip and count\$3 and compar\$5	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2006/02/24 11:05
S39	47	"714"/\$.ccls. and logic with analyzer with on-chip and debug\$4	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2006/02/24 11:06
S40	47	"714"/\$.ccls. and logic with analyzer with on-chip and debug\$4	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2006/02/24 11:07
S41	4	"714"/\$.ccls. and logic adj analyzer adj on-chip and debug\$4	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2006/02/24 11:09
S43	4	"714"/\$.ccls. and logic adj analyzer adj on-chip and @ad<"20011229"	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2006/02/24 11:12

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S42	4	"714"/\$.ccls. and logic adj analyzer adj on-chip and debug\$4 and @ad<"20011229"	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2006/02/24 11:12
S47	7	"714"/\$.ccls. and (logic near3 analyzers near5 on-chip OCLAs) and @ad<"20011229"	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	OFF	2006/02/24 11:14
S45	31	"714"/\$.ccls. and (logic near3 analyzer near5 on-chip OCLA) and @ad<"20011229"	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2006/02/24 11:14
S44	31	"714"/\$.ccls. and logic near3 analyzer near5 on-chip and @ad<"20011229"	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2006/02/24 11:14
S46	31	"714"/\$.ccls. and (logic near3 analyzers near5 on-chip OCLAs) and @ad<"20011229"	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2006/02/24 11:23
S49	52	"714"/\$.ccls. and (logic adj analyzers OCLAs) with chip and @ad<"20011229"	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	OFF	2006/02/24 11:24
S48	255	"714"/\$.ccls. and (logic adj analyzers OCLAs) and @ad<"20011229"	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	OFF	2006/02/24 11:24
S50	40	"714"/\$.ccls. and (logic adj analyzers OCLAs) near5 chip and @ad<"20011229"	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	OFF	2006/02/24 11:25
S51	22	"714"/\$.ccls. and (logic adj analyzers OCLAs) near3 chip and @ad<"20011229"	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	OFF	2006/02/24 11:27
S53	2	"714"/\$.ccls. and (logic adj analyzers OCLAs) near5 integrated adj circuit and @ad<"20011229"	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	OFF	2006/02/24 11:28
S52	1	"714"/\$.ccls. and (logic adj analyzers OCLAs) near3 integrated adj circuit and @ad<"20011229"	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	OFF	2006/02/24 11:28
S55	3	"714"/\$.ccls. and (logic adj analyzers OCLAs) with integrated adj circuit and @ad<"20011229"	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	OFF	2006/02/24 11:29

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S56	52	"714"/\$.ccls. and (logic adj analyzers OCLAs) with chip and @ad<"20011229"	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	OFF	2006/02/24 11:30
S54	2	"714"/\$.ccls. and (logic adj analyzers OCLAs) near5 integrated adj circuit and @ad<"20011229"	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	OFF	2006/02/24 11:30
S57	40	"714"/\$.ccls. and (logic adj analyzers OCLAs) near5 chip and @ad<"20011229"	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	OFF	2006/02/24 11:31
S58	36	"714"/\$.ccls. and (logic adj analyzers OCLAs) near5 chip and @ad<"20011228"	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	OFF	2006/02/24 11:37
S59	64	(logic adj analyzers OCLAs) near5 chip and debug\$4 and @ad<"20011228"	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	OFF	2006/02/24 11:39
S60	38	(logic adj analyzers OCLAs) near5 chip same debug\$4 and @ad<"20011228"	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	OFF	2006/02/24 11:41
S61	63	(logic adj analyzer OCLAs) near5 chip same debug\$4 and @ad<"20011228"	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2006/02/24 11:42
S62	4	(logic adj analyzer OCLAs) near5 chip near debug\$4 and @ad<"20011228"	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2006/02/24 11:44
S64	161	logic adj analyzers near5 chip and @ad<"20011228"	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2006/02/24 11:50
S68	43	logic adj analyzers near3 chip and @ad<"20011228"	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	OFF	2006/02/24 11:51
S67	64	logic adj analyzers near5 chip and @ad<"20011228"	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	OFF	2006/02/24 11:51
S65	33	logic adj analyzers near chip and @ad<"20011228"	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2006/02/24 11:51

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S69	9	logic adj analyzers near2 chip and @ad<"20011228"	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	OFF	2006/02/24 11:55
S73	22	324/755-765.ccls. and logic adj analyzers and @ad<"20011228"	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	OFF	2006/02/24 11:57
S72	22	logic adj analyzers with "IC" and @ad<"20011228"	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	OFF	2006/02/24 11:57
S74	176	(logic adj analyzer OCLAs) near5 debug\$4 and @ad<"20011228"	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2006/02/24 12:01
S63	9	(logic adj analyzer OCLAs) near5 chip near3 debug\$4 and @ad<"20011228"	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2006/02/24 12:01
S79	11	(logic adj analyzers OCLAs) near debug\$4 and @ad<"20011228"	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	OFF	2006/02/24 12:02
S78	11	(logic adj analyzers OCLA) near debug\$4 and @ad<"20011228"	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	OFF	2006/02/24 12:02
S77	29	(logic adj analyzers OCLA) near debug\$4 and @ad<"20011228"	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2006/02/24 12:02
S76	133	(logic adj analyzers OCLA) near3 debug\$4 and @ad<"20011228"	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2006/02/24 12:02
S75	2	(logic adj analyzer near chip OCLA) near3 debug\$4 and @ad<"20011228"	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2006/02/24 12:02
S80	11	LITT ADJ TIMOTHE.in.	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	OFF	2006/05/17 16:57
S81	7	S80 and logic with analyzer with chip and compar\$5 and count\$3	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	OFF	2006/05/17 16:58

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S18	69	"714"/\$.ccls. and logic with analyzer with chip and compar\$5 and count\$3	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	OFF	2006/05/17 16:58
S82	3	S80 and (logic with analyzer with chip and compar\$5 and count\$3).clm.	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2006/05/17 17:01
S84	53384	HEWLETT adj PACKARD.as.	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2006/05/17 17:02
S83	1	HEWLETT adj PACKARD.as. and (logic with analyzer with chip and compar\$5 and count\$3).clm.	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2006/05/17 17:02
S86	2	714/724.ccls. and (logic with analyzer with chip).clm.	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2006/05/17 17:03
S85	2	HEWLETT adj PACKARD.as. and (logic with analyzer with chip).clm.	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2006/05/17 17:03
S87	21	714/724.ccls. and logic with analyzer with chip	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2006/05/17 17:04